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ATTORNEYS AT LAW

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

RE: Inventor: Takashi MIYAMORI

Patent No.: 7,096,344

Issued: August 22, 2006

Group Art Unit: 2111

Examiner: Kenneth S. KIM

For: EXECUTING PARALLEL PROCESSOR IN DUAL /
SINGLE OPERATION INSTRUCTION MODES
WITH COPROCESSOR RECEIVING HALT SIGNAL

SIR:

Attached hereto for filing are the following papers:

REQUEST FOR CERTIFICATE OF CORRECTION
CERTIFICATE OF CORRECTION FORM PTO 1050

Our check in the amount of \$0.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

Fred L. Braun

Marvin J. Spivak

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Registration No. 56,123

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(OSMMN 03/06)

SEP 01 2006



Docket No.: 244612US2CONT



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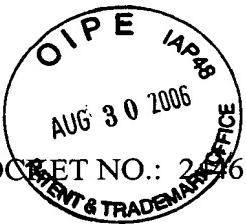
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DOCKET NO.: 200612US2CONT/rkj

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Takashi MIYAMORI

PATENT NO.: 7,096,344

GROUP: 2111

ISSUED: August 22, 2006

EXAMINER: Kenneth S. KIM

FOR: EXECUTING PARALLEL PROCESSOR IN DUAL / SINGLE OPERATION
INSTRUCTION MODES WITH COPROCESSOR RECEIVING HALT SIGNAL

REQUEST FOR CERTIFICATE OF CORRECTION

DIRECTOR OF THE UNITED STATES PATENT AND TRADEMARK OFFICE
ALEXANDRIA, VA 22313-1450

SIR:

The following is a Request for Certificate of Correction in Serial Number 10/695,996, now U.S. Patent Number 7,096,344.

In accordance with the provisions of Rule 322 of the Rules of Practice, which implement 35 USC 254, the U.S. Patent and Trademark Office is respectfully requested to issue a Certificate of Correction in the above-identified patent.

In light of the fact that the errors were the fault of the U.S. Patent and Trademark Office, no fees are required. The requested corrections are listed on FORM P.T.O. 1050.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : **7,096,344**
DATED: **Aug. 22, 2006**
INVENTOR(S): **Miyamori**

It is certified that an error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the title page, Item (54), and in Column 1 the Title is incorrect. Item (54) and Column 1 should read:

-- **EXECUTING PARALLEL PROCESSOR IN DUAL / SINGLE OPERATION
INSTRUCTION MODES WITH COPROCESSOR RECEIVING HALT
SIGNALS --**

Mailing address of sender:

Patent No. **7,096,344**

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : **7,096,344**

DATED: **Aug. 22, 2006**

INVENTOR(S): **Miyamori**

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